

T-XFP-CWDM-80KM**10Gbps XFP CWDM Transceiver 80KM****Features**

Wavelengths selectable to ITU-T standards covering CWDM grid wavelengths.

XFP MSA Rev 4.5 Compliant

Supports 9.95Gbps to 11.3Gbps bit rates

No reference Clock required

Cooled EML transmitter and APD receiver

Link Length of 80km

Low Power Dissipation 3.5W Maximum

XFI and Lineside Loopback Mode Supported

-5°C to 70°C Operating Case Temperature

Diagnostic Performance Monitoring of Module Temperature, Supply Voltages, Laser Bias Current,

Transmit Optical Power and Receive Optical Power

RoHS compliant

**Applications**

SONET OC-192&SDH STM 64 (with/without FEC)

CWDM 80KM 10G Ethernet

CWDM 80KM 10G Fiber Channel

CWDM 80km 10G Ethernet with FEC

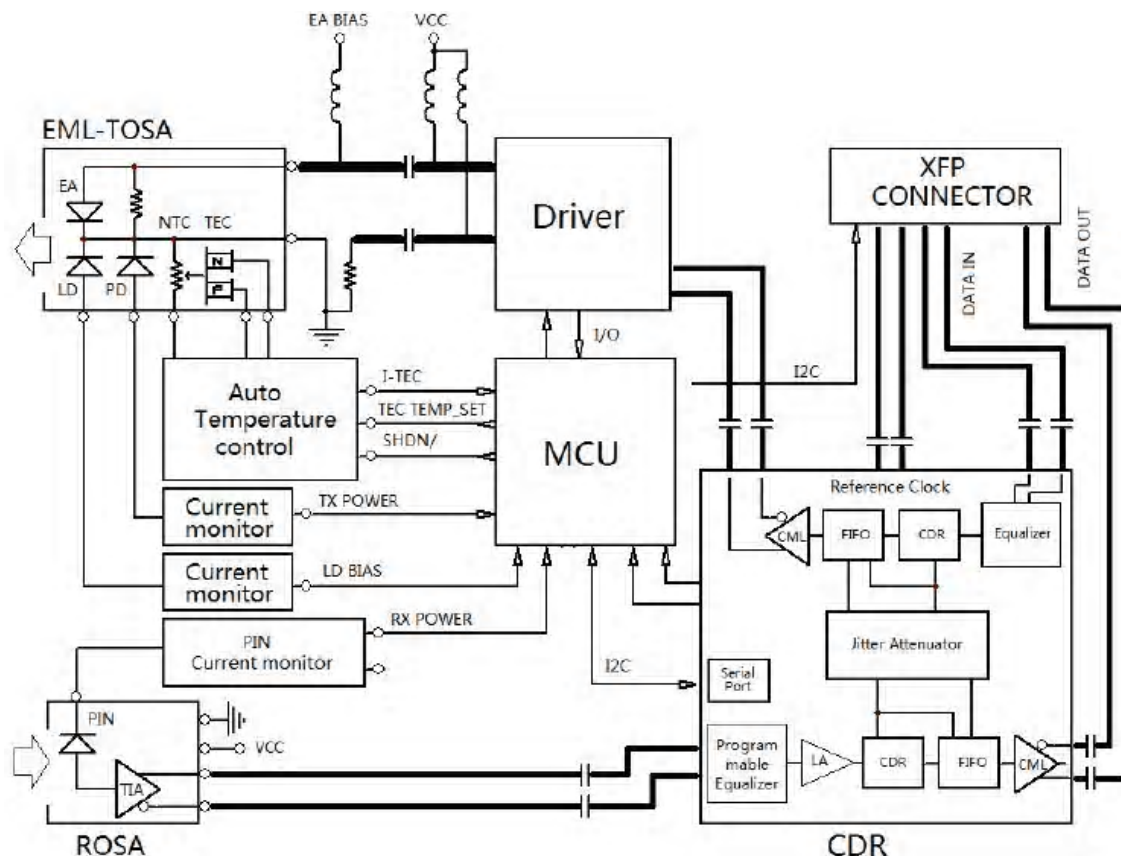
CWDM Networks

Description

The T-XFP-CWDM-80KM series single-mode transceiver exhibits excellent wavelength stability, supporting operation at 100GHz channel, cost effective module. It is designed for 10G CWDM SDH, 10GBASE-ZR and 10G Fiber-Channel applications.

The transceiver consists of two sections: The transmitter section incorporates a cooled EML laser. And the receiver section consists of a APD photodiode integrated with a TIA. All modules satisfy class I laser safety requirements. Sinton CWDM XFP transceiver provides an enhanced monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage.

Module Block Diagram



Regulatory Compliance

Feature	Agency	Standard	Certificate / Comments
Laser Safety	FDA	CDRH 21 CFR 1040 and Laser Notice No.50	1120292-000
Product Safety	UL	UL and CUL EN60950-2:2007	E347511
Environmental Protection	SGS	RoHS Directive 2002/95/EC	GZ1001008918/CHEM
EMC	WALTEK	EN55022:2006+A1:20077 EN55024:1998+A1+A2:2003	WT10093759-D-E-E

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage 1	Vcc3	-0.5	4.0	V
Supply Voltage 2	Vcc5	-0.5	6.0	V
Supply Voltage 3	Vcc2	-0.5	2	V
Storage Temperature	Tst	-40	85	°C
Case Operating Temperature	Top	-5	70	°C

Recommended Operating Conditions

Parameter	Symbol	Min.	Typical	Max.	Unit
Supply Voltage 1	Vcc3	3.13	3.3	3.47	V
Supply Current 1	Icc3			750	mA
Supply Voltage 2	Vcc5	4.75	5	5.25	V
Supply Current 2	Icc5			500	mA
Supply Voltage 3	Vcc2	1.71	1.8	1.89	V
Supply Current 3	Icc2			750	mA
Operating Case Temperature	Tca	-5		70	°C
Module Power Dissipation	Pm			3.5	W

Transmitter Specifications-Optical

Parameter	Symbol	Min.	Typical	Max.	Unit
Center Wavelength	λ_c	1464.5		1617.5	nm
Center Wavelength Stability	$\Delta\lambda_D$	-6.5	λ_c	6.5	nm
Optical Transmit Power	Po	0		+3	dBm
Optical Transmit Power(disable)	PTX_DIS			-30	dBm
Extinction Ratio	ER	9			dB
Jitter Generation (P-P)	JG P-P			0.1	UI
Jitter Generation(RMS)	JG RMS			0.01	UI
Spectral Width (-20dB)	$\Delta\lambda_{20}$			0.3	nm
Side Mode Suppression Ratio	SMSR	30			dB
Dispersion Penalty (800ps/nm) (2)	DP			2	dB
Relative Intensity Noise	RIN			-130	dB/Hz
Eye Mask	Compliant with ITU-T G.691 STM-64 eye mask				

Note:

1. Wavelength stability is achieved within 60 seconds (max) of power up.
2. BER=10⁻¹²; PRBS [2³¹-1@9.95Gbps](#).

Receiver Specifications-Optical

Parameter	Symbol	Min.	Typical	Max.	Unit
Maximum Input Power	RX-overload	-7			dBm
Input Operating Wavelength	λ_c	1528		1565	nm
Reflectance	Rrx			-27	dB
Loss of Signal Asserted	LOS_A	-34			dBm
LOS De-Asserted	LOS_D			-24	dBm
LOS Hysteresis	LOS_H	0.5			dB
Receiver Sensitivity					
Data Rate (Gb/s)	BER	Dispersion (ps/nm)	Sensitivity back to banck at OSNR> 30Db(dBm)	Power Penalty at OSNR>30Db(dBm)	
9.95~10.7	1e-12	-500 to 1450	-24	2	
Stressed Receiver Sensitivity in 10.3Gbps(OMA)	1e-12	-500 to 2400	-23	2	
OSNR Performance					
Data rates(Gb/s)	BER	Dispersion (ps/nm)	Min OSNR Back to back at power: -7 to -18dBm (dB)	Min OSNR Back to back at power: -7 to -18dBm (dB)	
9.95~10.7	1e-12	-500 to 1450	24	4	
11.1~11.3	1e-12	-500 to 1300	16	4	

Transmitter Specification-Electrical

Parameter	Symbol	Min.	Typ.	Max	Unit
Input Differential Impendence	R _{in}		100		Ω
Differential Data Input	V _{txDIFF}	120		850	mV
Transmit Disable Voltage	V _D	2.0		V _{cc3} +0.3	V
Transmit Enable Voltage	V _{en}	0		+0.8	V
Transmit Disable Assert Time	V _n			10	us

Receiver Specification-Electrical

Parameter	Symbol	Min.	Typ.	Max	Unit
Output Differential Impedance	Rom		100		Ω
Differential Output Swing	Vout P-P	350		850	mV
Rise/Fall Time	Tr/Tf	24			ps
Loss of Signal-Asserted	VOH	2		Vcc3+0.3	V
Loss of Signal-Negated	VOL	GND		GND+0.5	V

Note:

1. 20%-80%;

Pin Definition

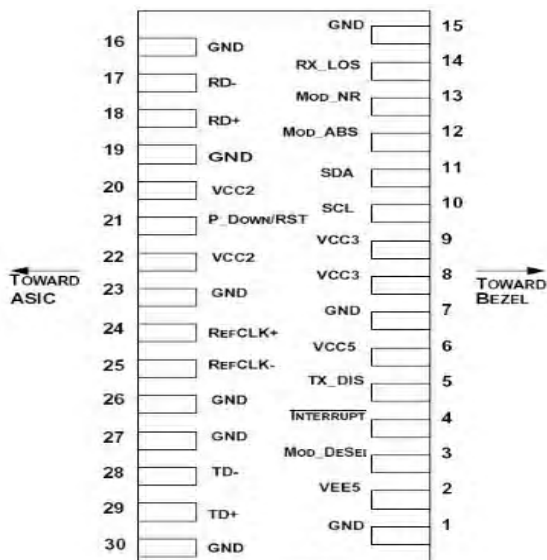
Parameter	Logic	Unit	Values	
1		GND	Module Ground	1
2		VEE 5	Optional-5.2 Power Supply-Not Required	
3	LVTTL-I	Mod-Desel	Module De-select; When held low allows the module to , respond to 2-wire serial interface commands	
4	LVTTL-O	Interrupt	Interrupt (bar); Indicates presence of an important condition which can be read over the serial 2-wire interface	2
5	LVTTL-I	TX-DIS	Transmitter Disable; Transmitter laser source turned off	
6		VCC5	+5 Power Supply – Not required	
7		GND	Module Ground	1
8		VCC3	+3.3V Power Supply	
9		VCC3	+3.3V Power Supply	
10	LVTTL-I	SCL	Serial 2-wire interface clock	2
11	LVTTL-I/O	SDA	Serial 2-wire interface data line	2
12	LVTTL-O	Mod_Abs-	Module Absent; Indicates module is not present. Grounded in the module.	2

13	LVTTTL-O	Mod_NR	Module Not Ready;	2
14	LVTTTL-O	RX_LOS	Receiver Loss of Signal indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RD-	Receiver inverted data output	
18	CML-O	RD+	Receiver non-inverted data output	
19		GND	Module Ground	
20		VCC2	+1.8V Power Supply	
21	LVTTTL-I	P_Down/R_ST	Power Down; When high, places the module in the low power stand-by mode and on the falling edge of P_Down initiates a module reset Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle.	
22		VCC2	+1.8V Power Supply	
23		GND	Module Ground	
24	PECL-I	RefCLK+	Reference Clock non-inverted input, AC coupled on the host board – Not required	
25	PECL-I	RefCLK-	Reference Clock inverted input. AC coupled on the host board – Not required	
26		GND	Module Ground	
27		GND	Module Ground	
28	CML-I	TD-	Transmitter inverted data input	
29	CML-I	TD+	Transmitter non-inverted data input	
30		GND	Module Ground	

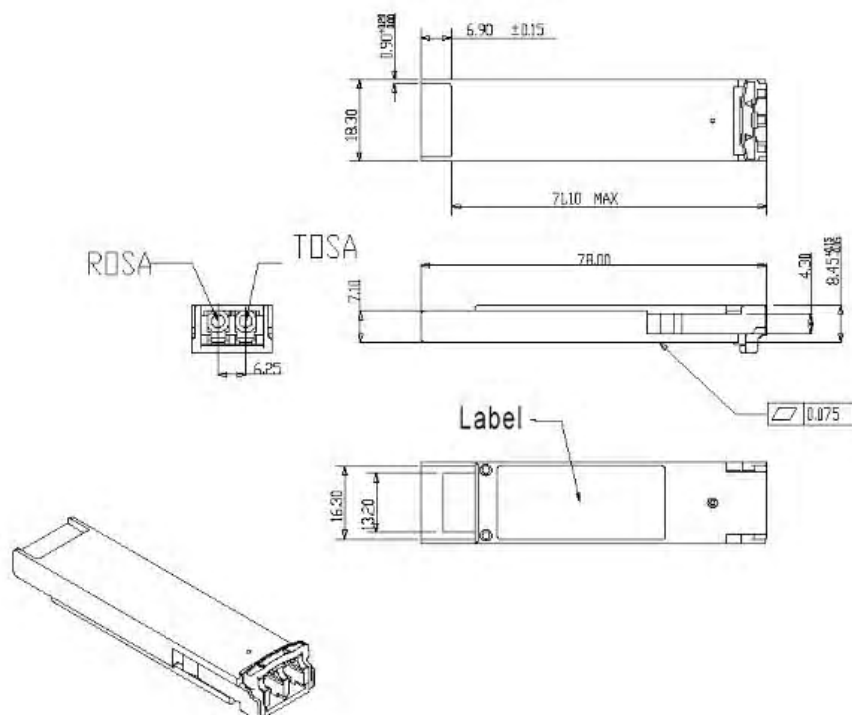
Notes:

1. Module circuit ground is isolated from module chassis ground within the module.
2. Open collector; should be pulled up with 4.7k – 10k ohms on host board to a voltage between 3.15V and 3.6V.
3. Reference Clock input is not required.

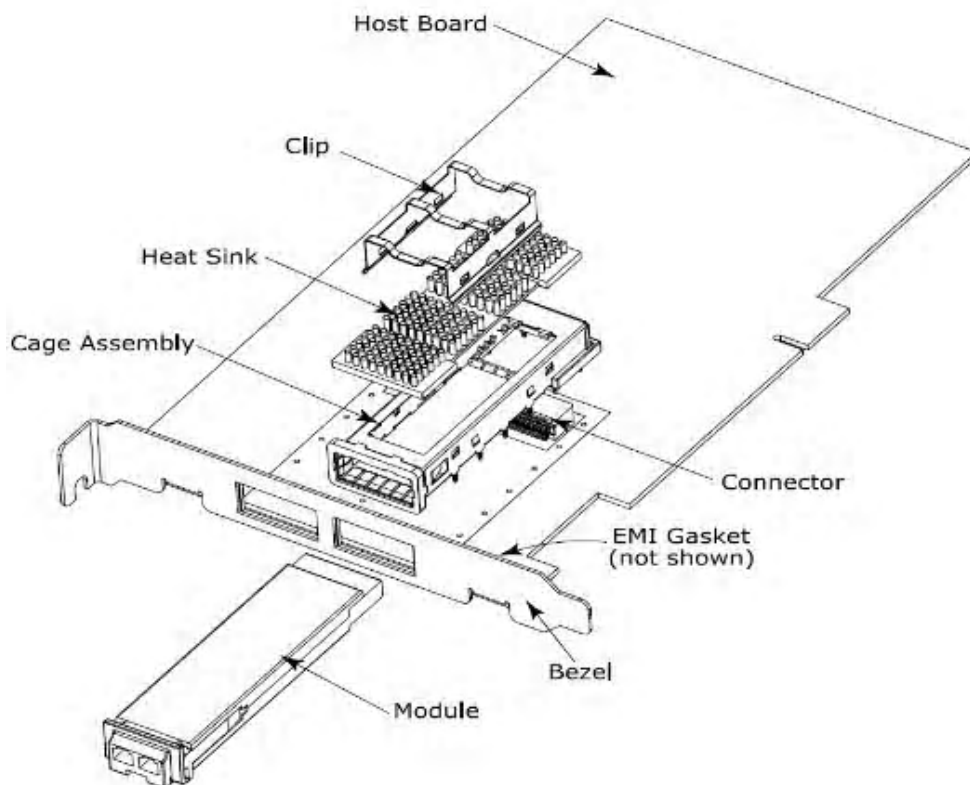
Electrical Pin-out Details



Mechanical Specifications



XFP Mechanical Components





10Gbps XFP CWDM Module

The mechanical components defined :

1. The module, clip and connector dimensions are constant for all applications. While the bezel, cage assembly, EMI gasket and heat sink can be designed and/or adjusted for the individual application.
2. The relatively small form factor of the XFP module combined with an adaptable heatsink option allows host system design optimization of module location, heatsink shape/dimension/fins design, and airflow control. The module can be inserted and removed from the cage with the heat sink and clip attached.

Ordering information

Part Number	Product Description
T-XFP-CWDM-80KM	9.95~10.3Gbps CWDM XFP 80km -5℃~+70℃ (1470~1610nm)

Notice:

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